Implementation of Network Reconfiguration Technique for Loss Minimization on a Standard 16 Bus Distribution System

Shivakumar L. N., Kiran Kumar G. R., Marulasiddappa H. B.

Abstract—Due to deregulation and restructuring it is expected that a large number of distributed generators will be connected to the distribution network. This will result in power distribution systems having locally looped networks and bi-directional power flows affecting existing operational schemes. The distribution system reconfiguration is one of the important control schemes which can be affected by the presence of distributed generators.

This paper presents a simple approach for distribution reconfiguration with distributed generators. The distributed generators are considered as constant power sinks and the loss minimization algorithm has usual constraints along with line capacity constraint which limits the reverse power flow of distributed generators. Standard switching indices are used for network reconfiguration and the algorithm is tested on a on a standard 16 bus system.

Index Terms— Distributed generation (DG), Network reconfiguration, Loss reduction

I. INTRODUCTION

The power system for many years was vertically and centrally operated. The electric power is transmitted and distributed to consumers over long distance transmission and distribution lines. With the impending deregulated environment faced by the electric utility industry and recent advances in technology, several distributed generation (DG) options are fast becoming economically viable. Some of the reasons for the sudden upsurge in the implementation of DG technologies are listed below [1].

- Deregulation of the electric utility industry.
- Public opposition to building new transmission lines on environmental grounds.
- Public awareness of the environmental impacts of electric power generation.
- Increase in public desire to promote “green” technologies.
- Awareness of the potential of DG to enhance the security of electric supply by creating mini and micro grids.

When regulations on power supply are relaxed and a number of DGs are introduced into the power system, unexpected problems may occur in power system operation and planning. DGs will increase the complexity of controlling, protecting and maintaining the distribution systems [2].

There are several operational schemes in power distribution systems and one of these is network reconfiguration.

Feeder reconfiguration for loss reduction is a very important function of automated distribution system to reduce distribution feeder losses and improve system security. There are a number of closed and normally opened switches in a distribution system, and the number of possible switching operations is tremendous. Thus feeder reconfiguration becomes complex decision making and time consuming process for system operators. Network reconfigurations basically modify the network structure of distribution feeders by changing the open/closed status of sectionalizing (normally closed) and tie (normally open) switches. To implement this process efficiently many algorithms have been developed [3-5]. With DGs, network reconfiguration becomes more complex since distribution network changes from single source to multiple sources.

II. PROBLEM FORMULATION

The reconfiguration is a complicated combinatorial and constrained optimization problem owing to the enormous number of candidates switching combinations in distribution systems. The problem of network reconfiguration is formulated as below.

From [3], the power loss in the series circuit element $i j$ between the buses $i$ and $j$ is given by

$$P_{loss(i,j)}=R_{ij}(\left|V_j-V_i\right| \left|Y_{ij}\right|)^2$$ (1)

Where

- $R_{ij}$: branch resistance between bus $i$ and bus $j$
- $V_i (V_j)$: bus $i (j)$ voltage
- $Y_{ij}$: branch admittance between bus $i$ and bus $j$

In general, network reconfiguration for loss minimization could be formulated as

$$\text{Min} \sum P_{loss(i,j)}=\sum R_{ij}(\left|V_j-V_i\right| \left|Y_{ij}\right|)^2$$ (2)

Satisfying the following constraints

- Radial network constraint: Distribution network should be composed of radial structure considering operational point of view.
- Isolation constraint: All nodes are energized.
- Voltage constraint: Voltage magnitude at each node must lie within the permissible limits.
- Current constraint: Current magnitude of each branch must lie within the permissible limits.
- In loss reduction with DG the reverse power of DG cannot exceed the corresponding line capacity limits.

III. SOLUTION METHODOLOGY

In network reconfiguration problems with DG, knowing the installation node and capacity of DG, reconfiguration could be solved by existing loss minimization algorithms. The reconfiguration is a complicated combinatorial and constrained optimization problem owing to the enormous number of candidates switching combinations in distribution systems. The problem of network reconfiguration is formulated as below.

From [3], the power loss in the series circuit element $i j$ between the buses $i$ and $j$ is given by

$$P_{loss(i,j)}=R_{ij}(\left|V_j-V_i\right| \left|Y_{ij}\right|)^2$$ (1)

Where

- $R_{ij}$: branch resistance between bus $i$ and bus $j$
- $V_i (V_j)$: bus $i (j)$ voltage
- $Y_{ij}$: branch admittance between bus $i$ and bus $j$

In general, network reconfiguration for loss minimization could be formulated as

$$\text{Min} \sum P_{loss(i,j)}=\sum R_{ij}(\left|V_j-V_i\right| \left|Y_{ij}\right|)^2$$ (2)

Satisfying the following constraints

- Radial network constraint: Distribution network should be composed of radial structure considering operational point of view.
- Isolation constraint: All nodes are energized.
- Voltage constraint: Voltage magnitude at each node must lie within the permissible limits.
- Current constraint: Current magnitude of each branch must lie within the permissible limits.
- In loss reduction with DG the reverse power of DG cannot exceed the corresponding line capacity limits.

III. SOLUTION METHODOLOGY

In network reconfiguration problems with DG, knowing the installation node and capacity of DG, reconfiguration could be solved by existing loss minimization algorithms. The reconfiguration is a complicated combinatorial and constrained optimization problem owing to the enormous number of candidates switching combinations in distribution systems. The problem of network reconfiguration is formulated as below.

From [3], the power loss in the series circuit element $i j$ between the buses $i$ and $j$ is given by

$$P_{loss(i,j)}=R_{ij}(\left|V_j-V_i\right| \left|Y_{ij}\right|)^2$$ (1)

Where

- $R_{ij}$: branch resistance between bus $i$ and bus $j$
- $V_i (V_j)$: bus $i (j)$ voltage
- $Y_{ij}$: branch admittance between bus $i$ and bus $j$

In general, network reconfiguration for loss minimization could be formulated as

$$\text{Min} \sum P_{loss(i,j)}=\sum R_{ij}(\left|V_j-V_i\right| \left|Y_{ij}\right|)^2$$ (2)

Satisfying the following constraints

- Radial network constraint: Distribution network should be composed of radial structure considering operational point of view.
- Isolation constraint: All nodes are energized.
- Voltage constraint: Voltage magnitude at each node must lie within the permissible limits.
- Current constraint: Current magnitude of each branch must lie within the permissible limits.
- In loss reduction with DG the reverse power of DG cannot exceed the corresponding line capacity limits.
reduction techniques. DGs are represented as constant power sinks and therefore they can be represented as negative load [5]. Taking this aspect into consideration the most appropriate switching combinations are determined using the switching indices developed from previous work [3]. The switching indices are obtained in the following manner. All the tie switches are closed in the network to form as many loops as the number of tie switches. In the meshed network each loop has a best opening point for minimum loss. By opening that switch radial topology in that loop is regained. The procedure is repeated for all other loops. The switching indices are obtained for all the branches in the looped state.

Switching Index= (Voltage index) * (Ohmic index) 

The voltage index

\[ \mu_V(n) = \exp \left( \frac{-\omega |\Delta V_n|^2}{(\Delta V_{av})} \right) \]  

\[ \Delta V_n : \text{the voltage drop between two terminals of Branch N} \]
\[ \Delta V_{av} : \text{the mean square voltage drop of all branches for a chosen loop} \]
\[ \omega : \text{weighing factor} \]

The ohmic index

\[ \mu_O(n) = \exp \left( -\omega \left( \frac{R_{av}Y_{av}}{R_{av}Y_{av}} \right) \right) \]  

\[ R_{av} : \text{the average branch resistance for a chosen loop} \]
\[ Y_{av} : \text{the average branch admittance for a chosen loop} \]
\[ \omega : \text{weighing factor} \]

The weighing factor \( \omega \) is such that the weightage to open a branch progressively decreases as we move away from the tie branch in either direction of the loop starting from the tie branch.

The switching indices are arranged in the descending order and those branches having largest index values are the candidate branches for reconfiguration. However, since the problem of reconfiguration is a complicated combinatorial and constrained optimization problem the best solution may not be obtained from opening the branches with largest index values. The best option could be further down in the order of switching indices and therefore it can be found by determining the losses for a selected number of options in the descending order of switching indices. The combination which gives the minimum loss is the best option.

Based on the above solution methodology a solution algorithm is presented and the flow chart is as shown in fig 1.

1. Read the network data, switch data, DG data and nodes of DG injection
2. At the nodes of DG injection, modify the load data with DGs as power sinks i.e., negative load.
3. Close all the switches to form meshed network and run AC load flow.
4. From the load flow data, compute the switching indices for all the branches of each loop, arrange them in the descending order.
5. Consider the loop near the source loop and open the branch with highest switching index and run the AC load flow.
6. Check for constraint violation.
7. If any constraint is violated, ignore that branch for opening and go to step 5.
8. Repeat steps 5, 6, 7 for a selected number of switching indices in the descending order and choose the switch with minimum power loss.
9. Retain the radial topology of the loop and repeat steps 5, 6, 7 and 8 for all the loops in the network.
10. Obtain the final reconfiguration report.

**IV. CASE STUDY AND NUMERICAL RESULTS**

Typical 16 bus system:
The developed algorithm has been applied to a standard 16 bus radial distribution system with DG injection. The Initial network without DG units is shown in figure 2 where the tie branches are S15, S21 and S26. The DG injection nodes and capacity are given in table I.

Fig. 1. Flow chart for reconfiguration with DG
TABLE I
INSTALLATION NODE AND CAPACITY OF DG

<table>
<thead>
<tr>
<th>Node</th>
<th>Capacity (MW/PF)</th>
</tr>
</thead>
<tbody>
<tr>
<td>9</td>
<td>7/0.8</td>
</tr>
</tbody>
</table>

The Optimum network without DG units, Initial network with DG units and Optimum network with DG units are shown in figures 3, 4 and 5 respectively.

Fig. 2. Initial network without DG units
Fig. 3. Optimum network without DG units
Fig. 4. Initial network with DG units
Fig. 5. Optimum network with DG units

From simulation results the open switches, losses and percentage loss reduction in each case are tabulated in the table II.

TABLE II
TEST RESULTS

<table>
<thead>
<tr>
<th>CASE</th>
<th>OPEN SWITCH</th>
<th>LOSS (MW)</th>
<th>LOSS REDUCTION (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Initial network without DG</td>
<td>S15, S21, S26</td>
<td>0.5114</td>
<td>-</td>
</tr>
<tr>
<td>Optimum network without DG</td>
<td>S17, S19, S26</td>
<td>0.4661</td>
<td>8.86</td>
</tr>
<tr>
<td>Initial network with DG</td>
<td>S15, S21, S26</td>
<td>0.2387</td>
<td>53.32</td>
</tr>
<tr>
<td>Optimum network with DG</td>
<td>S17, S19, S26</td>
<td>0.2289</td>
<td>55.24</td>
</tr>
</tbody>
</table>

From the results of the case study on IEEE 16 bus system, the loss reduction effects of DG units can be summarized as follows:

1. There is about 55% loss reduction in the optimized network with DG units as compared with the initial network without DG. A substantial improvement in feeder voltage levels can also be observed from the load flow studies. These prove the loss reduction support of DG units in distribution networks.

2. In order to maximize the loss reduction effects, the DG units should be integrated into the distribution automation center and they should be located at the most optimal locations.

V. CONCLUSION

This paper presents a simple approach for distribution reconfiguration with distributed generators. The distributed generators are considered as constant power sinks and therefore they are represented as negative loads. Switching indices are used for network reconfiguration. The switching indices are arranged in the descending order and those branches having largest index values are the candidate branches for reconfiguration.
However, since the problem of reconfiguration is a complicated combinatorial and constrained optimization problem the best solution may not be obtained from opening the branches with largest index values. The best option could be further down in the order of switching indices and therefore it can be found by determining the losses for a selected number of options in the descending order of switching indices. The combination which gives the minimum loss is the best option.

The developed algorithm is applied to a standard 16 bus system. It is interesting to note that there is about 55% loss reduction in the optimized network with DG units as compared with the initial network without DG.

APPENDIX

TABLE III

<table>
<thead>
<tr>
<th>Line</th>
<th>R (pu)</th>
<th>X (pu)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1-4</td>
<td>0.075</td>
<td>0.10</td>
</tr>
<tr>
<td>4-5</td>
<td>0.080</td>
<td>0.11</td>
</tr>
<tr>
<td>4-6</td>
<td>0.090</td>
<td>0.18</td>
</tr>
<tr>
<td>6-7</td>
<td>0.040</td>
<td>0.04</td>
</tr>
<tr>
<td>2-8</td>
<td>0.110</td>
<td>0.11</td>
</tr>
<tr>
<td>8-9</td>
<td>0.080</td>
<td>0.11</td>
</tr>
<tr>
<td>9-10</td>
<td>0.110</td>
<td>0.11</td>
</tr>
<tr>
<td>9-11</td>
<td>0.080</td>
<td>0.11</td>
</tr>
<tr>
<td>3-13</td>
<td>0.110</td>
<td>0.11</td>
</tr>
<tr>
<td>13-14</td>
<td>0.090</td>
<td>0.12</td>
</tr>
<tr>
<td>13-15</td>
<td>0.080</td>
<td>0.11</td>
</tr>
<tr>
<td>15-16</td>
<td>0.040</td>
<td>0.04</td>
</tr>
<tr>
<td>5-11</td>
<td>0.040</td>
<td>0.04</td>
</tr>
<tr>
<td>10-14</td>
<td>0.040</td>
<td>0.04</td>
</tr>
<tr>
<td>7-16</td>
<td>0.090</td>
<td>0.12</td>
</tr>
</tbody>
</table>

TABLE IV

<table>
<thead>
<tr>
<th>Load</th>
<th>P (MW)</th>
<th>Q (MVar)</th>
</tr>
</thead>
<tbody>
<tr>
<td>L4</td>
<td>2.0</td>
<td>1.6</td>
</tr>
<tr>
<td>L5</td>
<td>3.0</td>
<td>1.5</td>
</tr>
<tr>
<td>L6</td>
<td>2.0</td>
<td>0.8</td>
</tr>
<tr>
<td>L7</td>
<td>1.5</td>
<td>1.2</td>
</tr>
<tr>
<td>L8</td>
<td>4.0</td>
<td>2.7</td>
</tr>
<tr>
<td>L9</td>
<td>3.0</td>
<td>3.0</td>
</tr>
<tr>
<td>L10</td>
<td>1.0</td>
<td>0.9</td>
</tr>
<tr>
<td>L11</td>
<td>0.6</td>
<td>0.1</td>
</tr>
<tr>
<td>L12</td>
<td>4.5</td>
<td>2.0</td>
</tr>
<tr>
<td>L13</td>
<td>1.0</td>
<td>0.9</td>
</tr>
<tr>
<td>L14</td>
<td>1.0</td>
<td>0.7</td>
</tr>
<tr>
<td>L15</td>
<td>1.0</td>
<td>0.9</td>
</tr>
<tr>
<td>L16</td>
<td>2.1</td>
<td>1.0</td>
</tr>
</tbody>
</table>

REFERENCES


AUTHORS PROFILE

Shivakumar LN received his B.E. degree (Electrical Engineering) in 2008 from VTU. M.Tech.(Power Systems) in 2010 from VTU. He joined the department of Electrical & Electronics Engineering PESITM, in 2010 and is currently Assistant Professor in PESITM, Shimoga. His research interests are in the area of power system, control systems. He has published several papers in national journals. He is a life time member of MISTE.

Kiran Kumar GR received his B.E. degree (Electrical Engineering) in 2009 from VTU. M.Tech.(Computer Application in Industrial Drives) in 2011 from VTU. He joined the department of Electrical & Electronics Engineering HMSIT, in 2011 and is currently Assistant Professor in PESITM, Shimoga. His research interests are in the area of high voltage, control systems. He has published several papers in national journals. He is a life time member of MISTE.

Marulasiddappa HB received his B.E. degree (Electrical Engineering) in 2005 from UBDT. He joined the department of Electrical & Electronics Engineering UBDT,Davangere in 2005, also received his M.Tech. degree (Power Electronics) in 2009 from UBDT. He joined the department of Electrical & Electronics Engineering PESITM, in 2010 and is currently Assistant Professor in PESITM, Shimoga. His research interests are in the area of Power Electronics, control systems. He has published several papers in national journals. He is a life time member of MISTE.