Dynamic Characterization and Noise Analysis of 4H-SiC Impatt Diode at Ka Band

Joydeep Sengupta, Girish Chandra Ghivela, Monojit Mitra

Abstract—The microwave as well as the small signal noise properties on a one dimensional n++p++ DDR structure 4H-SiC IMPATT Diode have been studied using advanced computer simulation program developed by us and compared at different frequency of Ka band by taking the area of the diode as $10^{-2}m^2$. Also the theory for the diode current noise associated with the electron hole pair generation and recombination in the space charge region of the diode is presented. This paper can help to know about the small signal behavior as well as noise behavior of IMPATT diode along with power density at the Ka band and will be helpful for designing the 4H-SiC based IMPATT diode depending upon the microwave applications.

Index Terms—Impact ionization, efficiency, mean square noise voltage, quality factor, noise spectral density, power density, noise measure.

1. INTRODUCTION
In recent years, IMPATT(Impact Avalanche and Transit Time) diodes have proved as a major source of microwave and millimetre wave power generator. Thus, designers are concentrating on the best semiconductor materials for the optimized design of the diode. For our design, we have choose 4H-SiC material for the IMPATT because of high electron mobility and have recently been established as technologically important materials for both electronic and optoelectronic devices[1]-[2]. Also the advantages of SiC-based IMPATT devices over traditional Si, GaAs, InP based IMPATT at Ka-band were presented in [3]. However, the noise generated by the statistical nature of impact ionization in the active space charge region limits the potential of IMPATT diode as a microwave generator for various applications[4]. So, the understating of the noise generation mechanism and its behaviour at small signal conditions is very important in establishing optimized design and operating conditions for IMPATT diodes.

II. SIMULATION METHOD
The three stage computer simulation method using MATLAB consists of DC analysis, small signal analysis and noise analysis are carried out on a one-dimensional n++p++ DDR model of 4H-SiC based IMPATT diode throughout the Ka band. The device structure is shown in figure1. The equations involved in the analysis are nonlinear in nature, and thus their solutions are more complex. So the IMPATT diode is considered as consisting of several small space points for the analysis. The active layer width of the diode is divided into number of steps each of size 0.055 nm.

Figure1: The Active Layers of a Reverse Biased p-n Junction

1 DC ANALYSIS
To obtain the DC parameters such as break down voltage, carrier current profiles, electric field profiles etc., dc analysis is done here by solving Poisson equation, the space charge equation and the carrier continuity equation simultaneously over the double drift structure of the diode[5]. The values of the material parameters of 4H-SiC are taken as a standard one, which are enlisted in table1 and the design parameters are shown in table 2. The Computer simulation method for DC analysis is initiated from the position $X_0$ of the field maximum $E_m$ in the depletion layer which is situated near the metallurgical junction. In the location of the field maximum i.e. at $x=X_0$, where $dE/dx=0$, the value of the maximum electric field $E_m$ and its location $X_0$ in the depletion layer are initially chosen suitably for the taken doping profile and dc current density($J_0$)(table1(B)). Then by using this the initial value of the mobile space charge $P(X_0)$ is determined. After that the Poisson equation and the carrier continuity equation are solved simultaneously through the numerical approach[6] by taking space steps of very small width(0.055nm). Iteration over $E_m$ and $X_0$ are carried out till boundary conditions are being satisfied at both edges (at $x=X_0$ and W) of the depletion layer. The numerical approach is started from the point $x=X_0$ (field maxima) and moving towards the right side of the junction(p-region) till the field boundary conditions, in carrier current and electric field are satisfied at $x=W$. Now the computation again started at field maximum point and it proceed towards the left side of the junction (n-region) till boundary condition is satisfied. Thus, the DC electric field and carrier current distribution profiles
for the 4H-SiC IMPATT diode operating at the taken current density \((4 \times 10^8 \, \text{Am}^{-2})\) are obtained from the final solutions of the \(E_n\) and \(X_0\). The method described above gives the avalanche breakdown characteristics of the IMPATT diode. The avalanche layer width \(X_A\) is obtained by taking the condition \(P(x) = 0.95\), i.e. 95% growth of the electron and hole carrier current. The operating frequency of IMPATT diode essentially depends on the transit time of the charge. Computer simulation using MATLAB is carried out at operating frequencies of 26.5 GHz to 40 GHz and the width of the epilayers are accordingly chosen using the transit time formula [Sze and Ryder,(1971)[7]] \(W_{n,p} = 0.35 \times V_{n,p} / f\); where \(W_{n,p}\) is the penetration of depletion layer towards n and p-sides, \(V_{n,p}\); are the saturation voltage for electron, hole respectively and \(f\) is the operating frequency. The depletion layer width of the diode is obtained as \(W = |W_n| + |W_p|\) and drift region width is obtained as \(W - X_A\). The voltage drop across the diode zones, i.e. breakdown voltage of the diode \(V_b\) and avalanche voltage drop \(V_A\) are determined by integrating the electric field over the corresponding zone layer. Then, the voltage drop across the drift zone is calculated by \(V_p = V_b - V_A\). The DC to RF conversion efficiency of the diode can be obtained from the expression,

\[
\eta(\%) = \frac{2mV_D}{\pi V_b}
\]

where \(m\) is the modulation index and the breakdown voltage is calculated by integrating the spatial field profile over total depletion layer width as then the results obtained from the DC analysis are used in the small signal analysis.

\[
V_b = \int_0^W E(x) \, dx
\]

**Table1 (A,B):** Material parameters used for the analysis (at 300K) \((A)\)

<table>
<thead>
<tr>
<th>(A_n) ((\times 10^9 , \text{m}^{-1}))</th>
<th>(B_n) ((\times 10^9 , \text{m}^{-1}))</th>
<th>(A_p) ((\times 10^9 , \text{m}^{-1}))</th>
<th>(B_p) ((\times 10^9 , \text{m}^{-1}))</th>
<th>(V_{ns}) ((\times 10^9 , \text{m}^{-1}))</th>
<th>(V_{ps}) ((\times 10^9 , \text{m}^{-1}))</th>
</tr>
</thead>
<tbody>
<tr>
<td>4.57</td>
<td>5.24</td>
<td>5.13</td>
<td>1.57</td>
<td>2.508</td>
<td>2.5</td>
</tr>
</tbody>
</table>

**Table1 (B):** Material parameters used for the analysis (at 300K) \((B)\)

<table>
<thead>
<tr>
<th>(J_0) ((\times 10^8 , \text{Am}^{-2}))</th>
<th>(N_D) ((\times 10^{21} , \text{m}^{-3}))</th>
<th>(N_A) ((\times 10^{21} , \text{m}^{-3}))</th>
<th>(\mu_n) ((\text{m}^2 \text{Vs}^{-1}))</th>
<th>(\mu_p) ((\text{m}^2 \text{Vs}^{-1}))</th>
</tr>
</thead>
<tbody>
<tr>
<td>4</td>
<td>2.8</td>
<td>2.9</td>
<td>0.12</td>
<td>0.12</td>
</tr>
</tbody>
</table>

**2 SMALL SIGNAL ANALYSIS**

The high frequency performance of the IMPATT diode can be obtained by doing the small signal analysis of the diode. The DC parameters such as electric field profile and current profile, which are obtained from the DC analysis, are fed as input data for the small signal analysis. In small signal condition the ac field developed across the diode is assumed to be much smaller than the breakdown field. As a result the ionization rates with electric field can be assumed to be linear. Therefore, small signal solutions can be obtained by linearizing the complex diode equations. The drift velocities of the charge carriers are assumed to be saturated and the diffusion current is not considered in the small signal analysis, but considered in the noise analysis. The small signal parameters like negative conductance \((-G)\), susceptance \((B)\), the diode impedance \(Z(x, \omega)\), quality factor \((Q_p)\) and range of frequency over which the diode exhibit the negative conductance can easily be computed numerically through the Gummel-Blue approach, where the model takes into account, the contribution from each space points and effectively determines the parameters of the diode after satisfying the boundary conditions. The real part \(R(x, \omega)\) and imaginary part \(X(x, \omega)\) are obtained by splitting the diode impedance \(Z(x, \omega)\) using Gummel-Blue method and thus two different equations are framed \([8-11]\). Then by using modified Runge-Kutta method the solutions of these two equations are found through a double iterative simulation scheme.

The diode negative resistance \((-Z_R)\) and reactance \((-X_X)\) are computed through numerical integration of the \(-R(x)\) and \(-X(X)\) profiles over the active space charge layer. Thus

\[
-Z_R = \int_0^W -R(x) \, dx \quad \text{and} \quad -X_X = \int_0^W -X(x) \, dx
\]

The diode admittance \(Y\) is obtained by,

\[
Y = \int_0^W \frac{Z(x, \omega)}{dx} = \frac{1}{Z_R + jX_X}
\]

And

\[
-G = \frac{Z_R}{Z_R^2 + Z_X^2}
\]

\[
B = \frac{Z_X}{Z_R^2 + Z_X^2}
\]

The small signal quality factor \((Q_p)\) is calculated by

\[
-Q_p = \frac{B}{-G}
\]

The expected RF power delivery from the diode can be calculated by using the relation.
\[ P_{RF} = (V_{RF})^2 |G_p| \times A / 2 \]  
\[ P_A \]  

The power density \( P_A \) is taken as \( P_{RF} / A \), where \( A \) is the area of the diode. Under small signal condition \( V_{RF} \) can be approximated as \( V_B / 2 \) for 50% modulation. \( G_p \) is the device negative conductance at peak frequency.[4].

3 SMALL SIGNAL NOISE ANALYSIS

The generation of noise in IMPATT diode arises mainly from the statistical nature of the generation of electron-hole pairs in the avalanche region. The disturbances in the electric field distribution, voltage and current is due to the random nature of impact ionization process. These fluctuations are assumed as the constituent of shot noise current fluctuation due to avalanching multiplication process and considered as small signal in nature[12]-[13]. Since the noise fix the lower limit to the microwave signals to be amplified, it is important to study the effect of noise and its characteristic along the diode structure is computed using the simulation program in our analysis[14]-[16]. The noise characteristic like noise spectral density i.e. mean square noise voltage per bandwidth \(<V^2> / df \) and noise measure (NM) of the diode are computed from this analysis.

Let \( \gamma_N(x') \) be the noise generation rate at \( x' \) (position of noise source) in the generation region giving rise to mean square noise current deviation over a frequency interval \( df \) as

\[ <dl_N^2> = \int 2qdl_N(x')df = 2q^2 Adf \gamma_N(x')dx' \]  
\[ \text{in the space interval x of the depletion layer;} \]

\[ dl_N(x') = Aq \gamma_N(x') \]  

The noise source \( \gamma_N(x') \) located at \( x' \) in the avalanche region, generates a noise electric field \( e_n(x,x') \) at every point in the depletion region of the diode. The noise electric field \( e_n(x,x') = e_{NR}(x,x') + je_{NX}(x,x') \) contributed from each space point is computed by solving two simultaneous second order equations on \( e_{NR}(x,x') \) and \( e_{NX}(x,x') \) at each space point

\[ D^2e_{NR}(x,x') + [\alpha_r(x)-\alpha_p(x)]De_{NR}(x,x') = \frac{2r_p}{\bar{v}} De_{NX}(x,x') \]

\[ + \frac{\alpha_r^2}{2\bar{v}^2} - H(x)e_{NR}(x,x') - \frac{2\bar{\alpha}(x)\omega}{\bar{v}} e_{NX}(x,x') = \frac{2qr_p}{\bar{ve}} \gamma_N(x') \]  

\[ \text{and} \]

\[ D^2e_{NX}(x,x') + [\alpha_r(x)-\alpha_p(x)]De_{NX}(x,x') + \frac{2r_p}{\bar{v}} De_{NR}(x,x') \]

\[ + \frac{\alpha_r^2}{2\bar{v}^2} - H(x)e_{NX}(x,x') + \frac{2\bar{\alpha}(x)\omega}{\bar{v}} e_{NR}(x,x') = 0 \]

Here

\[ D = \frac{\partial}{\partial x}, H = (\alpha_r' - \alpha_p')De_{NR} + \frac{2\alpha' j}{\bar{ve}}, \bar{v} = (\sqrt{\nu_n - \nu_p}), \]

\[ r_n = \frac{V_n - V_p}{2\nu}, r_p = \frac{V_n + V_p}{2\nu}, \alpha = \frac{\nu_n + \nu_p}{2\nu} \]

Initially, the noise generation source is assumed to be located at the left-hand edge of the active zone at \( x'_1 \), where \( i=1 \). Iterations over the initial chosen values of \( e_{NR}(x,x') \) and \( e_{NX}(x,x') \) in the left hand edge of the diode depletion layer are carried out till the boundary conditions are satisfied at the right hand edge. The profiles of noise field \( e_{NR}(x,x') \) and \( e_{NX}(x,x') \) can be obtained from the solutions of equations(8) and (9). This method is repeated to get the integrated noise field profile and noise voltage due to noise source successively located at \( x = x'_i + ndx' \), where \( n=2,3,4,...... \). Thus, the noise generation along the complete depletion layer can be obtained separately for each noise source located in various space points of the active zone. Thus, the terminal voltage produced due to noise generating source \( \gamma_N(x') \) located at \( x' \) is computed through the relation

\[ V_N(x') = \int e_n(x,x')dx = \int e_{NR}(x,x')dx + j \int e_{NX}(x,x')dx \]

\[ (10) \]

Then by shifting the noise source to \( x = x'_i \) for \( i=2,3,4,5...... \); real part \( V_{Rk}(x') \) and imaginary part \( V_{Ik}(x') \) of the terminal voltage are calculated for individual noise sources. These values of \( V_{Rk}(x') \) and \( V_{Ik}(x') \) are then integrated for noise sources consider along the entire depletion layer.

\[ \text{So, } V_R = \int V_{Rk}(x')dx' \text{ and } V_i = \int V_{Ik}(x')dx' \]

\[ (11) \]

From this the transfer impedance of the diode can be calculated as

\[ Z_i(x') = \frac{V_i(x')}{dl_N(x')}, Z_{Ik}(x') \]

\[ = \frac{V_{Rk}(x')}{q\gamma_N(x')Adx' + j \frac{V_{Ik}(x')}{q\gamma_N(x')Adx'}} \]

\[ (12) \]

Now, the mean square noise voltage can be determined by

\[ <V^2> = \int \int Z_i(x')^2 \gamma_N(x')dx'dx' \]

\[ (13) \]

and the noise measure (NM) of the diode as

\[ \text{NM} = \frac{<V^2> / df}{4KT(-Z_R)} \]

\[ (14) \]

where \( K \) is the Boltzmann constant, \( T \) is the absolute temperature and \((-Z_R)\) is the total negative diode resistance.

III. RESULTS AND DISCUSSIONS

The electric field profile \( E(x) \) and normalized current density profile \( P(x) \) of the 4H-SiC based DDR structure IMPATT diode have been obtained and optimized at different frequencies of the Ka-band by using the DC simulation program developed by the authors. The optimized

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design parameters of the design diode are summarized in table 2 and the efficiency, diode negative conductance, susceptibility, small signal quality factor and power density obtained by small signal analysis are tabulated in table 3.

### Table-2: Optimised design parameters (at 36 GHz and 300K)

<table>
<thead>
<tr>
<th>$E_m$ ($\times 10^3$ Vm$^{-2}$)</th>
<th>$V_D$ (V)</th>
<th>$V_B$ (V)</th>
<th>$V_A$ (V)</th>
<th>$\eta$ (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.963</td>
<td>29.929</td>
<td>71.407</td>
<td>41.477</td>
<td>26.672</td>
</tr>
</tbody>
</table>

### Table3: Results of small signal analysis and small signal noise analysis (at 300K)

<table>
<thead>
<tr>
<th>$f$ (GHz)</th>
<th>$G$ ($\times 10^7$ Sm$^{-2}$)</th>
<th>$B$ ($\times 10^7$ Sm$^{-2}$)</th>
<th>$Q_p$</th>
<th>$P_{RF}$ / $A$ ($\times 10^{10}$)</th>
<th>$&lt;V^2/\Delta f&gt;$ ($\times 10^{-15}$)</th>
<th>$V^2$-sec</th>
<th>$NM$ (db)</th>
</tr>
</thead>
<tbody>
<tr>
<td>26.5</td>
<td>-1.964</td>
<td>1.573</td>
<td>0.801</td>
<td>1.241</td>
<td>4.742</td>
<td>18.32</td>
<td></td>
</tr>
<tr>
<td>28</td>
<td>-2.237</td>
<td>1.704</td>
<td>0.761</td>
<td>1.421</td>
<td>2.333</td>
<td>21.01</td>
<td></td>
</tr>
<tr>
<td>30</td>
<td>-2.586</td>
<td>2.284</td>
<td>0.883</td>
<td>1.644</td>
<td>0.932</td>
<td>23.93</td>
<td></td>
</tr>
<tr>
<td>32</td>
<td>-2.615</td>
<td>2.451</td>
<td>0.937</td>
<td>1.662</td>
<td>0.205</td>
<td>30.13</td>
<td></td>
</tr>
<tr>
<td>34</td>
<td>-2.619</td>
<td>2.557</td>
<td>0.976</td>
<td>1.666</td>
<td>0.691</td>
<td>24.68</td>
<td></td>
</tr>
<tr>
<td>36</td>
<td>-2.622</td>
<td>2.610</td>
<td>0.995</td>
<td>1.671</td>
<td>1.895</td>
<td>20.22</td>
<td></td>
</tr>
<tr>
<td>38</td>
<td>-2.431</td>
<td>2.978</td>
<td>1.224</td>
<td>1.548</td>
<td>2.352</td>
<td>18.62</td>
<td></td>
</tr>
<tr>
<td>40</td>
<td>-2.238</td>
<td>3.097</td>
<td>1.383</td>
<td>1.426</td>
<td>4.742</td>
<td>17.71</td>
<td></td>
</tr>
</tbody>
</table>

### IV. CONCLUSIONS

The simulation results provides a clear view of conductance-susceptance profile as well as variation of mean square noise voltage, noise measure, power density and small signal quality factor throughout the Ka band. The results shows that at 36 GHz the DC to RF conversion efficiency of the diode is 26.672% , G-B value best optimized, Power handling capacity per unit area is more $1.671 \times 10^{10}$ Wm$^{-2}$ and noise measure(20.22 db) is also in moderate level as compared to other frequency in the Ka-band. So, at the 36 GHz the operation of IMPATT diode is satisfactory considering all those parameters. The analysis of this paper may be considered to be extremely helpful for the design of...
compound semiconductor based DDR IMPATT diode at Ka band for many civilian and military applications. As from this paper we are getting a clear cut idea about the effect of noise at Ka band. So the efficiency of the IMPATT diode can be improved by choosing proper operating frequency.

REFERENCES


Mr. Joydeep Sengupta is Assistant Professor of Electronics Engineering department at Visvesvaraya National Institute of Technology, Nagpur, Maharastra. He obtained his B.Tech degree from University of Kalyani, M.Tech degree from Burdwan University, West Bengal. His Ph.D is in progress in E&T department at BESU. His research interests are microwave & millimeter-wave semiconductor devices & systems. He is working with design, fabrication and characterization of wide band gap semiconductor based IMPATT diodes in Ka-band for long time.

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Dr. Monojit Mitra did his B.Tech and M.Tech in Radio Physics and Electronics from Calcutta University in 1982 and 1984, he was awarded Ph.D. by Calcutta University in 1995. He was awarded H. L. Mitter Gold Medal as stood 1st in Physics honors from Calcutta University, S. N. Ghosh Silver Medal as stood faculty 1st amongst all the departments and National scholarship throughout the studentship. Presently he is working as a Head of the Department of Electronics and Telecommunication Engineering, Bengal Engineering and Science University, Shibpur, West-Bengal. He had published four books so far, one on “Microwave Semiconductor Devices”(PHI) in 2003, another on “Satellite Communication”(PHI) in 2005, another on “Microwave Engineering (3rd Ed.”)(Dhanpat Rai) in 2006, and the last one on “Microwave & Radar Engineering” (Sci-tech-in press) published in 2013. Over the last 25 years he is doing research in the field of Fabrication of Microwave semiconductor device IMPATT and developed microwave sources for the solid state radars using the fabricated diode. At present he is also doing research on collision avoidance system for vehicles using MIMO radar system at microwave frequencies. So far 3 students got their Ph.D. degree under his supervision and about 12 students are currently doing their Ph.D. work under his guidance. He had published more than 80 research papers in different reputed national and international journals. He is also member of different prestigious organizations.