

Power Flow Controller/Short Circuit Current Limiter

G.Akhilesh, V.Suresh, P.Anvesh

Abstract— *This paper presents the various advantages of Thyristor Controller Series Capacitor (TCSC), both as a Power Flow Controller, as well as a Short Circuit limiter during faults. The results have been derived and verified from the software PSCAD, and the graphs and calculations are included in the paper. The results and information included in the paper are sufficiently accurate.*

Keywords- Modelling, power system dynamic stability.

I. INTRODUCTION

THYRISTOR-CONTROLLED SERIES CAPACITOR (TCSC) is a series FACTS device which allows rapid and continuous changes of the transmission line impedance. It has great application potential in accurately regulating the power flow on a transmission line, damping inter-area power oscillations, mitigating sub synchronous resonance (SSR) and improving transient stability.

The characteristics of a TCSC at steady-state and very low frequencies can be studied using fundamental frequency analytical models [1], [2]. These particular models recognize the importance of having different approach from SVC modeling (assuming only line current as a constant) which, although more demanding on the derivation, gives the most accurate TCSC model.

The fundamental frequency models cannot be used in wider frequency range since they only give the relationship among fundamental components of variables when at steady-state. Conventionally, the electromechanical transient programs like EMTP or PSCAD/EMTDC are used for TCSC transient stability analysis. These simulation tools are accurate but they employ trial and error type studies only, implying use of a large number of repetitive simulation runs for varying parameters in the case of complex analysis or design tasks. On the other hand, the application of dynamic systems analysis techniques or modern control design theories would bring benefits like shorter design time, optimization of resources and development of new improved designs. There have been a number of attempts to derive an accurate analytical model of a TCSC that can be employed in system stability studies and controller design [3]–[6]. The model presented in [3] uses a special form of discretization, applying Poincare mapping, for the particular Kayenta TCSC installation. The model derivation for a different system will be similarly tedious and the final model form is not convenient for the application of standard stability studies and controller design theories especially not for larger systems.

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A similar final model form is derived in [4], and the model derivation is improved since direct discretization of the linear system model is used, however, it suffers other shortcomings as the model in [3]. The modeling principle reported in [5] avoids discretization and stresses the need for assuming only line current as an ideal sine, however, it employs rotating vectors that might be difficult to use with stability studies, and only considers the open loop configuration. The model in [5] is also oversimplified because of the use of equivalent reactance and equivalent capacitance that might be deficient when used in wider frequency range. Most of these reported models are therefore concerned with a particular system or particular type of study, use overly simplified approach and do not include control elements or phase-locked loops (PLLs). In this study, we attempt to derive a suitable linear continuous TCSC model in state-space form. The model should have reasonable accuracy for the dynamic studies in the sub synchronous range and it should incorporate common control elements including PLL. To enable flexibility of the model use with different ac systems, the model structure adopts subsystem units interlinked in similar manner as with SVC modeling [6].

We also seek to offer complete closed-loop model verification in the time and frequency domains.

II. TEST SYSTEM

The test system for the study is a long transmission system compensated by a TCSC and connected to a firm voltage source on each side. Fig. 1 shows a single line diagram of the test system where the transmission line is represented by a lumped resistance and inductance in accordance with the approach for sub-synchronous resonance studies [7]. Each phase of the TCSC is composed of a fixed capacitor in parallel with a thyristor-controlled reactor (TCR). The TCSC is controlled by varying the phase delay of the thyristor firing pulses synchronized through a PLL to the line current waveform.

Three different test systems are used in order to verify the model accuracy with different system parameters and different operating points:

- System 1—75% compensation, capacitive mode;
- System 2—40% compensation, capacitive mode;
- System 3—75% compensation, inductive mode.

The test system parameters are practically selected according to the recommendations in [8] (regarding and natural resonance) and the test system data are given in the Appendix.

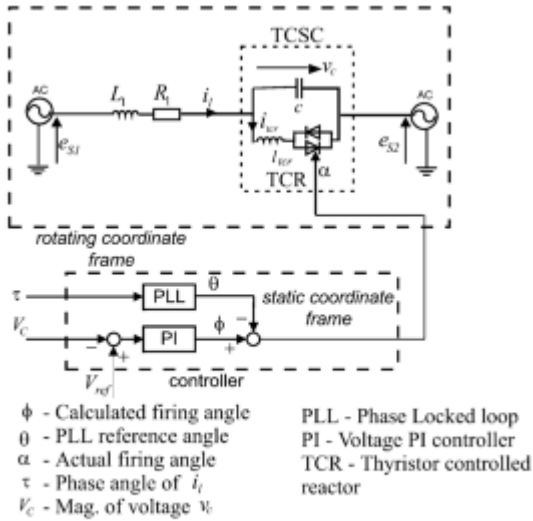


Fig. 1. Test system configuration.

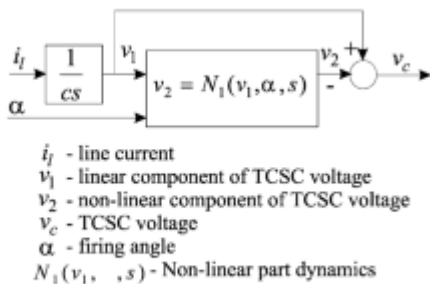


Fig. 2. TCSC model structure.

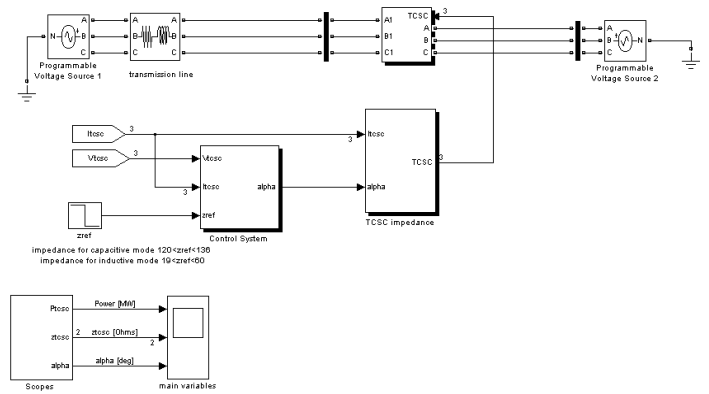
III. FUNDAMENTAL FREQUENCY MODEL

The fundamental frequency model of a TCSC is derived first to enable initialization of the steady-state parameters. The voltage across the TCSC capacitor comprises an uncontrolled and a controlled component [8], and it is presumed that the line current is constant over one fundamental cycle in accordance with [1], [2], [5]. The uncontrolled component, is a sine wave (unaffected by thyristor switching) and it is also constant over a fundamental cycle since it is directly related to the amplitude of the prevailing line current. The controlled component is a nonlinear variable that depends on circuit variables and on the TCR firing angle.

In this study, the controlled component is represented as a nonlinear function of the uncontrolled component and firing angle, as shown in Fig. 2. With this approach, captures the nonlinear phenomena caused by thyristor switching influence and all internal interactions with capacitor voltage assuming only that the line current and are linear. We seek in our work to study dynamics of in a wider frequency range and also to offer a simplified representation for fundamental frequency studies. The fundamental components of reactor current and the voltages and are selected as state variables and the non- linear state-space model is presented as

$$sv_1 = \frac{1}{c}i_l \tag{1}$$

$$sv_2 = g\frac{1}{c}i_{tcr} \tag{2}$$



Simulation Circuit for TCSC (500 KV System)

$$si_{tcr} = g\frac{1}{l_{tcr}}v_1 - g\frac{1}{l_{tcr}}v_2 \tag{3}$$

$$v_c = v_1 - v_2 \tag{4}$$

where represents the switching function for thyristor in conduction, and for thyristor in blocking state.

To derive the corresponding linear model at the nominal Frequency, it is postulated that the model has the following structure:

$$sv_1 = \frac{1}{c}i_l \tag{5}$$

$$sv_2 = \frac{1}{c}i_{tcr} \tag{6}$$

$$si_{tcr} = k_1v_1 - k_2v_2 \tag{7}$$

$$v_c = v_1 - v_2 \tag{8}$$

Simulation circuit for TCSC voltage phasor model (sub system)

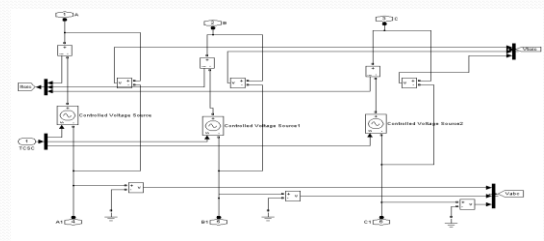


Fig 4 : Voltage Phasor Model

where $k_1 = k_1(\alpha)$ and $k_2 = k_2(\alpha)$ are the unknown model parameters dependent on the firing angle α . The above model structure is correct for zero firing angle ($g = 1, \alpha = 0$, i.e., full conduction with measured from the voltage crest) and we presume the same model structure but different parameter values for $\alpha \in (0, 90)$.

Considering the two components of the thyristor current on the right side of (7) we note that k_1v_1 produces the current that is driven by a constant voltage v_1 (over one fundamental cycle) in accordance to the earlier assumption. As a result, the configuration is similar to the shunt connection of TRC in a SVC, and the constant k_1 can be calculated using the approach of equivalent reactance X_{tcr} for



the TCR current in a SVC [6], [8]

$$1/k_1 = I_{alp} = \frac{I_{TCR}\pi}{\pi - 2\alpha - \sin(\pi - 2\alpha)} \quad (9)$$

In order to determine the constant k_2 , we represent (6)–(7) as a transfer function in the following form:

$$\frac{v_2(s)}{v_1(s)} = T_{f_o}(s) = \frac{k_3}{s^2/\omega_d^2 + 1} \quad (10)$$

with

$$\omega_d = \sqrt{\frac{k_2}{c}}, \quad k_3 = \frac{k_1}{k_2} \quad (11)$$

Observing (10), it is seen that ω_d is the resonant frequency of an undamped second order system. The proposed expression for this frequency is given below in (12) and it is determined using the experimental frequency response of the model for the nonlinear TCSC segment as presented later in Section IV

$$\omega_d = \frac{\pi - 2\alpha}{\pi \sqrt{I_{TCR}c}} \quad (12)$$

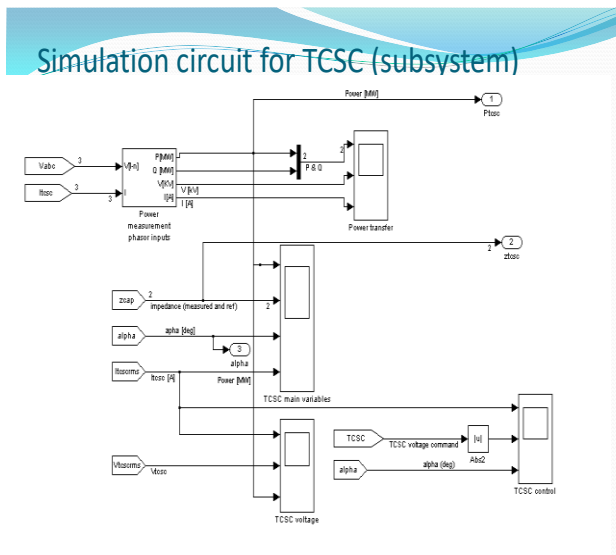


Fig 5: Simulation Circuit for TCSC.

TCSC gain at fundamental frequency is derived

$$\frac{v_c}{i_t} = \frac{(j\omega_o)^2/\omega_d^2 + 1 - k_3}{c j\omega_o [(j\omega_o)^2/\omega_d^2 + 1]} \quad (13)$$

TCSC fundamental frequency impedance is

$$X_{TCSC} = \frac{\omega_o^2/\omega_d^2 + k_3 - 1}{c\omega_o (1 - \omega_o^2/\omega_d^2)} \quad (14)$$

Where

$$k_3 = \frac{(\pi - 2\alpha)[(\pi - 2\alpha) - \sin(\pi - 2\alpha)]}{\pi^2 I_{TCR}^3/2 c^3/2} \quad (15)$$

and ω_d is defined in (12).

Compared with the fundamental frequency TCSC model in [1], [2], the derived model (13) is considerably simpler, yet it will be shown that the accuracy is very similar.

The above model is tested against PSCAD in the following way. The system is operated in open loop, i.e., with a fixed reference thyristor firing angle, in the configuration of Fig. 1. The value of the reference thyristor angle is changed in interval $\alpha \in (0, 90^\circ)$ and for each angle the value of the TCSC voltage is observed. Since all other parameters are constant, the TCSC voltage is directly proportional to the TCSC impedance and this is an effective way to obtain accurate information on the fundamental TCSC impedance. Fig. 3 shows the steady-state TCSC voltage against the range of firing angle values, where the above linear model results are shown as model 2 and also the results from researchers [1], [2] as model 1. It is seen that the proposed model shows good accuracy across the entire firing angle range and especially the results are very close to those from [1], [2]. In fact the above model differs negligibly from the model [1], [2], except in the less used low firing angles in range $\alpha \in (20, 30^\circ)$ where the difference is still below 5%. It is also evident that the two analytical models show small but consistent discrepancy against PSCAD/EMTDC, and despite all simplifications in PSCAD it was not possible to obtain better matching.

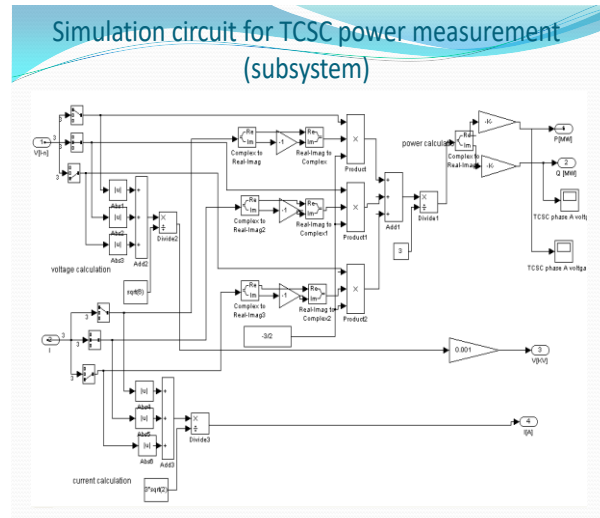


Fig 6: TCSC Power measurement.

IV. SMALL-SIGNAL DYNAMIC ANALYTICAL MODEL

A. TCSC Model

The transfer function in (10) is accurate only at fundamental frequency and it cannot be used for wider frequency studies. The goal of the dynamic modeling is to derive a dynamic expression of satisfactory accuracy in the sub-synchronous frequency range and for small signals around the steady-state operating point. The steady-state for an ac system variable is a periodic waveform at fundamental frequency with constant magnitude and phase.

The frequency response of TCSC is studied assuming that the input voltage is

$$v_1 = A_0 \sin(2\pi f_o t) + A_{inj} \sin(2\pi f_{inj} t) \quad (16)$$

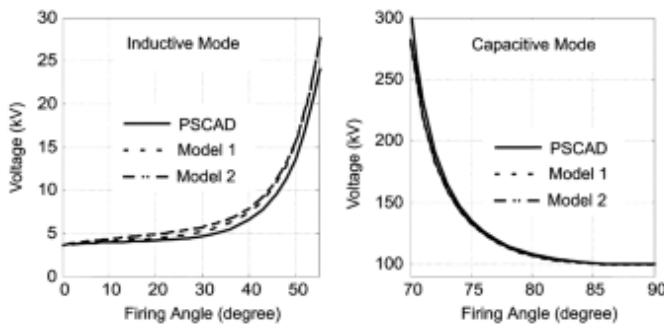


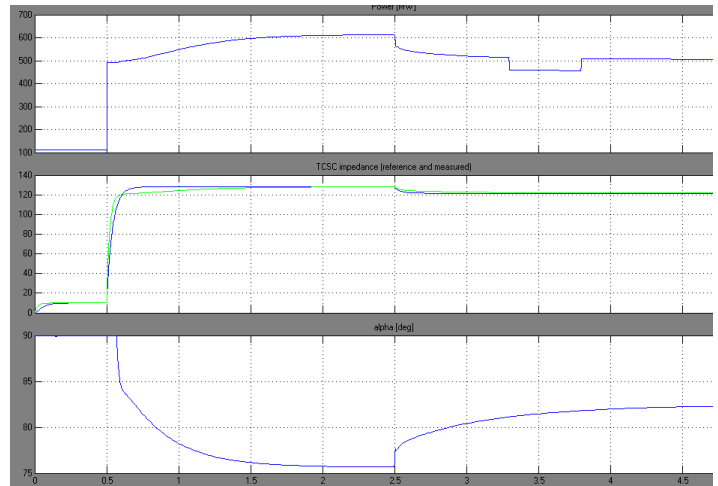
Fig 7 Steady State TCSC voltage for different firing angles.

where the first *sine* signal denotes the steady-state operation with all parameters constant. The second term is the input in the experimental frequency response and it has small magnitude. The output is monitored and the first harmonic of the Fourier series is compared with the input signal to obtain the frequency response.

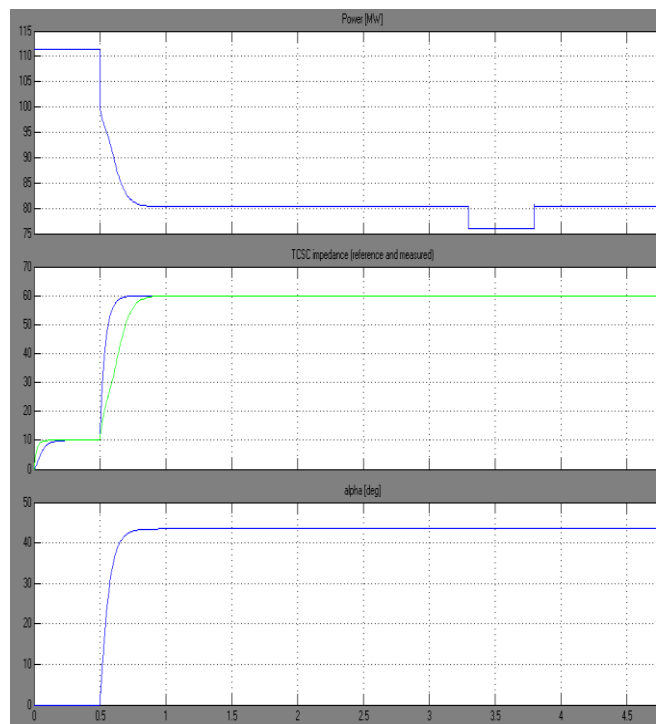
If it is assumed that the injected component is small in magnitude compared to the fundamental component, (as it is case in a small-signal study) the thyristor firing pulses will remain synchronized to the fundamental component. We can therefore base the study on the following assumptions:

- The firing pulses are regularly spaced (i.e., unaffected by the injected oscillations).
- The conduction period is symmetrical and unaffected by the injected signal.
- The magnitude of the fundamental frequency does not affect frequency response at other frequencies (the magnitude of fundamental component will be assumed zero in the study and, therefore, does not contain the fundamental component). Source generates a sine signal with the above range of frequencies and for each frequency the magnitude and phase of the first harmonic of the output voltage is observed.

The pulse generator produces equally spaced conduction intervals that are based on the fundamental frequency period and which are dependent on the firing angle. Based on the above assumption, these firing instants are unaffected by the injection signal.



TCSC in Inductive Mode



Power flow with TCSC in Inductive Mode

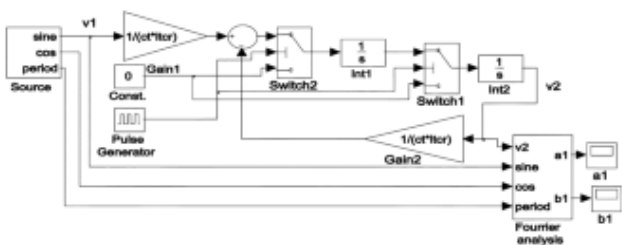


Fig. 4. Experimental frequency response set-up with the SIMULINK model for the nonlinear TCSC segment.

Test System (500 KV Transmission Line)

TCSC in Capacitive Mode

$$w_d = \frac{\pi - 2\alpha}{\pi \sqrt{I_{cr}c}} \tag{18}$$

$$\zeta_d = 0.38 \cos(\alpha) \tag{19}$$

$$w_n = \frac{\pi f_o 10^{-3}}{\sqrt{I_{cr}c}} \left(1.7 + \sqrt{10} \frac{\alpha}{\tan^4(\alpha)} \right) \tag{20}$$

$$\zeta_n = 0.2 \cos(\alpha). \tag{21}$$

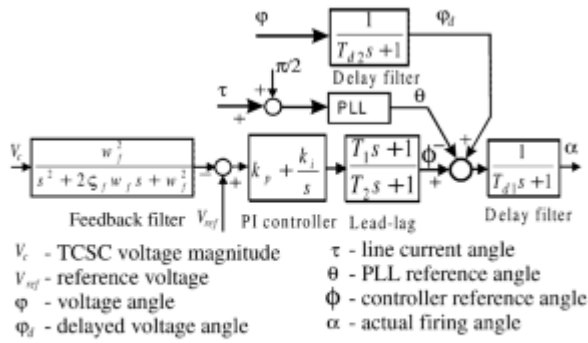


Fig. 8. Controller model and firing angle calculation.

The model (17) is next transferred to the state-space domain

$$\begin{aligned}
 s x_1 &= i_t / c \\
 s x_2 &= a_{tc23} x_3 \\
 s x_3 &= a_{tc31}(\alpha) x_1 + a_{tc32}(\alpha) x_2 + a_{tc33}(\alpha) x_3 \\
 s x_4 &= a_{tc41}(\alpha) x_1 + a_{tc42}(\alpha) x_2 + a_{tc43}(\alpha) x_3 + a_{tc44} x_4 \\
 v_c &= x_4
 \end{aligned} \quad (22)$$

where the parameters are

$$\begin{aligned}
 a_{tc23} &= 1 \\
 a_{tc31}(\alpha) &= \omega_d^2 \quad a_{tc32}(\alpha) = -\omega_d^2 \quad a_{tc33}(\alpha) = -\omega_d s_d \\
 a_{tc41}(\alpha) &= \frac{1}{T_f} \left(1 - \frac{\omega_d^2}{\omega_n^2} \right) \quad a_{tc42}(\alpha) = \frac{1}{T_f} \left(\frac{\omega_d}{\omega_n^2} - 1 \right) \\
 a_{tc43}(\alpha) &= \frac{1}{T_f} \left(\frac{s_d \omega_d}{\omega_n^2} - \frac{s_n}{\omega_n} \right) \quad a_{tc44} = \frac{1}{T_f}
 \end{aligned} \quad (23)$$

and the states are defined as

$$\begin{aligned}
 x_1 &= v_1, \quad x_2 = v_1 / \left(\frac{1}{\omega_d^2} s^2 + \frac{s_d}{\omega_d^2} s + 1 \right) \\
 x_3 &= s x_2, \quad x_4 = \frac{v_1 - v_2}{T_f s + 1}
 \end{aligned} \quad (24)$$

B. Model Connections

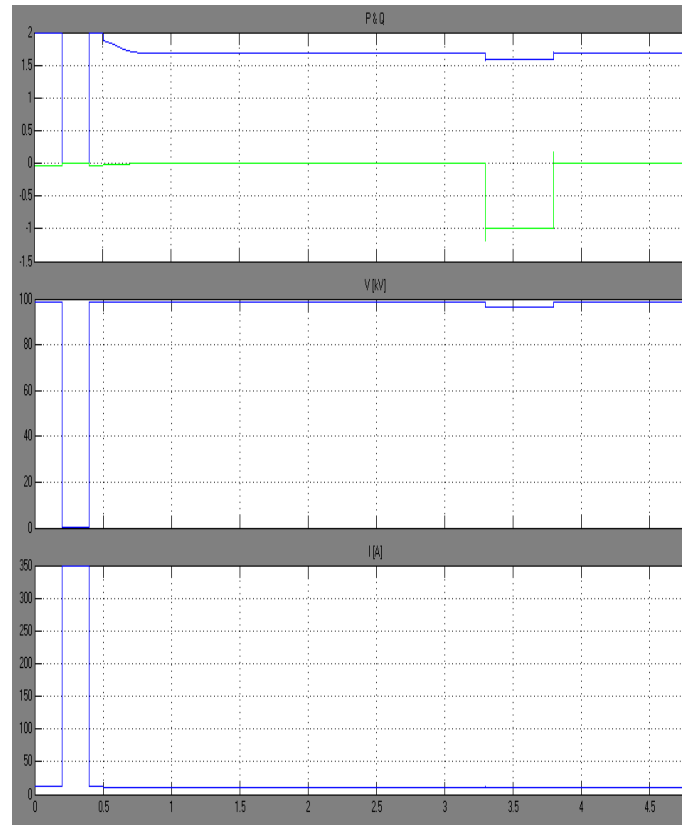
$$\begin{aligned}
 s \underline{x}_s &= A_s \underline{x}_s + B_s \underline{u}_{out} \\
 \underline{y}_{out} &= C_s \underline{x}_s + D_s \underline{u}_{out}
 \end{aligned} \quad (25)$$

where "s" labels the overall system, and the model matrices are

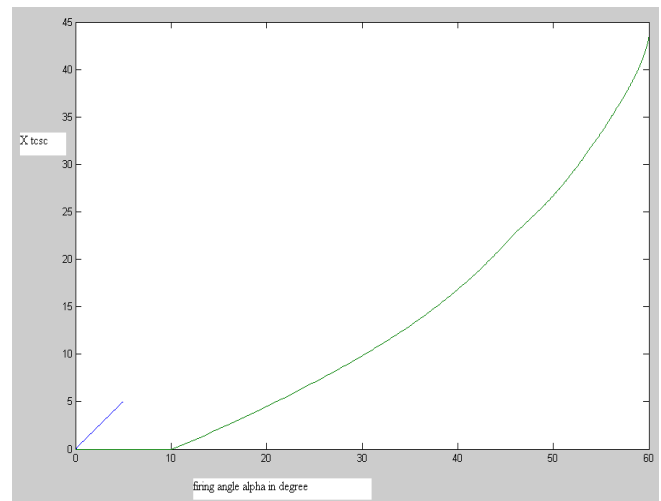
$$\begin{aligned}
 A_s &= \begin{bmatrix} A_{co} & B_{cotc} * C_{teco} & B_{coac} * C_{acco} \\ B_{teco} * C_{cotc} & A_{tc} & B_{teac} * C_{actc} \\ B_{acco} * C_{coac} & B_{actc} * C_{teac} & A_{ac} \end{bmatrix} \\
 B_s &= \begin{bmatrix} B_{coout} \\ B_{teout} \\ B_{acout} \end{bmatrix} \\
 C_s &= [C_{coout} \quad C_{teout} \quad C_{acout}] \\
 D_s &= [0].
 \end{aligned} \quad (26)$$

V. TEST RESULTS

Power Flow and Line Current under fault without TCSC.



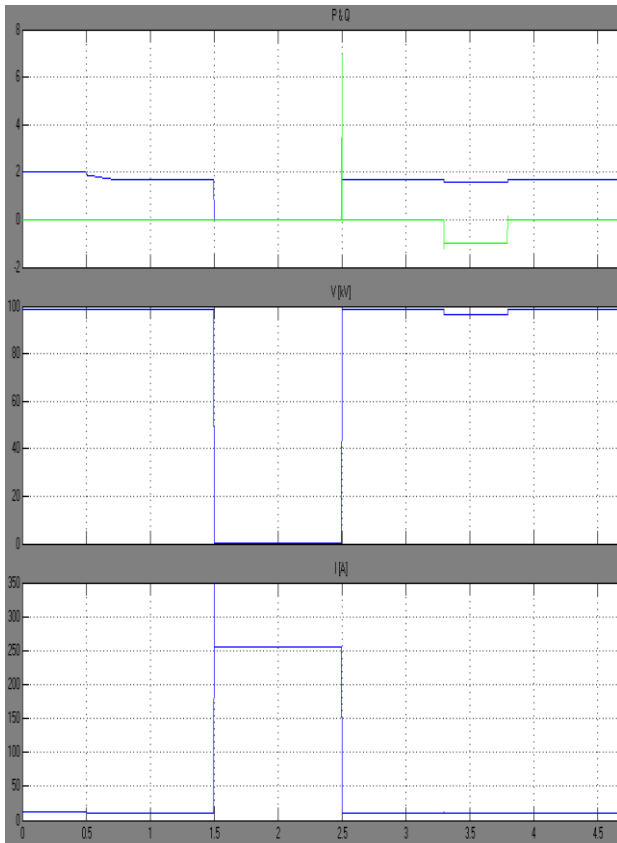
Here the fault is applied at 0.2 sec and cleared at 0.4 sec.



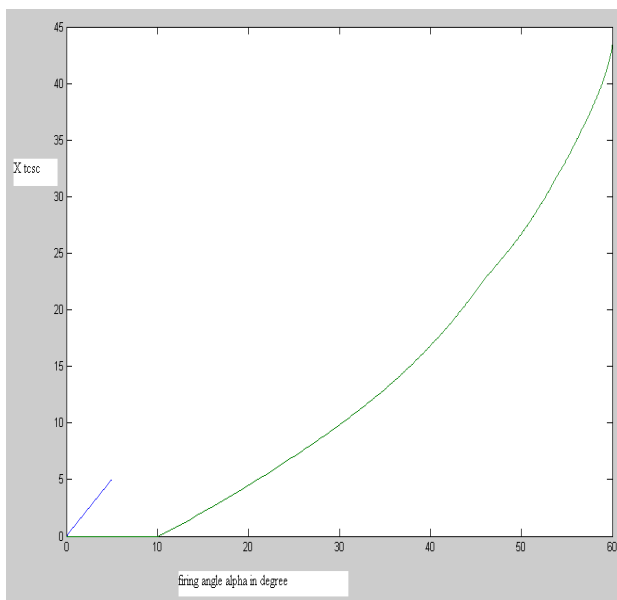
Plot : X(TCSC) versus firing angle in Inductive mode.

Power Flow Controller/Short Circuit Current Limiter

Power Flow and Line current under fault with TCSC



Here the fault is applied at 1.5 sec and cleared at 2.5 sec.



Plot : X(TCSC) versus firing angle in Capacitive mode.

VI. CONCLUSION

TCSC is placed on a 500kV, long transmission line, to improve power transfer. Without the TCSC the power transfer is around 110MW, as seen during the first 0.5s of the simulation when the TCSC is bypassed. The TCSC is modeled as a voltage source using equivalent impedance at

fundamental frequency in each phase. The nominal compensation is 75%, i.e. assuming only the capacitors (firing angle of 90deg). The natural oscillatory frequency of the TCSC is 163Hz, which is 2.7 times the fundamental frequency.

The TCSC can operate in capacitive or inductive mode, although the latter is rarely used in practice. Since the resonance for this TCSC is around 58deg firing angle, the operation is prohibited in firing angle range 49deg - 69deg. Note that the resonance for the overall system (when the line impedance is included) is around 67deg. The capacitive mode is achieved with firing angles 69-90deg. The impedance is lowest at 90deg, and therefore power transfer increases as the firing angle is reduced. In capacitive mode the range for impedance values is approximately 120-136 Ohm. This range corresponds to approximately 500MW power transfer range. Comparing with the power transfer of 110 MW with an uncompensated line, TCSC enables significant improvement in power transfer level.

TCSC reduces the short circuit current from 350amp to 250amp under three phase short circuit fault. So TCSC significantly reduces the short circuit current.

APPENDIX

TABLE I
TEST SYSTEM DATA

	System 1	System 2	System 3
AC system data			
R_l	6.0852 Ω	6.0852 Ω	6.0852 Ω
L_l	0.4323 H	0.4323 H	0.4323 H
e_{S1}	539 kV	539 kV	539 kV
e_{S2}	477.8 kV	477.8 kV	477.8 kV
f_o	60Hz	60Hz	60Hz
TCSC data (at nominal point)			
%comp($\alpha=90$)	75%	40%	75%
X_f/X_c	0.1343	0.1632	0.1343
$\omega_r = 1/\sqrt{I_{TCSC}c}$	163.7 Hz	148.54 Hz	163.7 Hz
c	21,977 μ F	41 μ F	21,977 μ F
I_{TCSC}	0.043 H	0.028 H	0.043 H
V_c	128.5 kV	28 kV	4.2 kV
Fir. angl. α	76°	72°	22°
Controller data			
k_p	-0.002rad/kV	-.006 rad/kV	0.05 rad/kV
K_I	-.06rad/(kVs)	-0.7rad/(kVs)	20rad/(kVs)
T_{d1}	1/220s	1/220s	1/220s
T_{d2}	1/1400s	1/1400 s	1/1400s
ζ_f	0.9	0.9	0.9
ω_f	50 Hz	50 Hz	20 Hz
PLL k_p	30	30	30
PLL k_I	300 1/s	300 1/s	300 1/s
T_1	0.03 s	0.0 s	0.0 s
T_2	0.007 s	0.0 s	0.0 s

REFERENCES

- 1) Dragan Jovicic and G. N. Pillai, "Analytical Modelling of TCSC Dynamics", IEEE Transactions on Power Delivery, VOL 20, No.2, APRIL 2005

- 2) S. G. Jalali, R. A. Hedin, M. Pereira, and K. Sadek, "A stability model for the advanced series compensator (ASC)," IEEE Trans. Power Del., vol. 11, no. 2, pp. 1128–1137, Apr. 1996.
- 3) C. R. Fuerte-Esquivel, E. Acha, and H. Ambriz-Perez, "A thyristor controlled series compensator model for the power flow solution of practical power networks," IEEE Trans. Power Syst., vol. 9, no. 15, pp. 58–64, Feb. 2000.
- 4) D. Jovic, N. Pahalawaththa, M. Zavahir, and H. Hassan, "SVC dynamic analytical model," IEEE Trans. Power Del., vol. 18, no. 4, pp. 1455–1461, Oct. 2003.